REMARKS/ARGUMENTS

Claims 1-39 stand rejected in the outstanding Official Action. Claims 1 and 20 have been amended and therefore claims 1-39 remain in the application.

The Examiner's acknowledgment of Applicants' claim for foreign priority and receipt of the certified copies of the priority documents is very much appreciated. Additionally, the Examiner's consideration of the previously submitted Information Disclosure Statement and attached references is appreciated.

The Examiner contends that Applicants' original title was not indicative of the invention to which the claims are directed. Applicants have amended the title to reflect that the invention is for the monitoring of at least two domains of a multi-domain processor. However, should the Examiner believe another title to be more clearly indicative of the invention, Applicants will certainly consider such suggestion.

Claims 1-16, 18, 20-34, 36, 38 and 39 stand rejected under 35 USC §102 as being anticipated by Jackson (U.S. Publication 2002/0188831). Applicants have amended method claim 1 to more positively recite the fact that the processor is monitored in "each of said at least two domains." Apparatus claim 20 has been amended to more positively recite the monitoring logic for monitoring the processor operating in "each of said first and second domains." Accordingly, this feature is positively recited in Applicant's claims and it is incumbent upon the Examiner to demonstrate how and where Applicants' claimed method steps and apparatus elements are not only shown in the Jackson reference, but also shown as being combined in the manner of Applicants' independent claims 1 and 20.

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As discussed in Applicants' specification, existing systems are known in which monitoring of two separate domains, one secure and the other unsecure, is known. However, the problem with such systems, as discussed on page 3, paragraph 2 of the present specification, is that when the entire system, i.e., both domains are monitored, there is a potential data leakage between the domains. The present invention serves to prevent data leakage between domains where both domains are controllably monitored by preventing monitoring of one domain when a predetermined condition is present and the related control value indicates that the monitoring function is not allowable.

The Jackson reference, as considered by the Examiner, discloses the monitoring function of a processor where the processor is operable in at least two domains where the domains are considered to be distinguished by a trace enable bit. However, Applicants' independent claims 1 and 20 both state that while monitoring is possible in both domains, it is controlled with respect to at least one of the domains. In Jackson, the system relates to transaction tracing and is not concerned with data leakage between domains, nor the ability to limit monitoring functions under certain conditions.

The Examiner interprets Jackson and its teaching of domains to be a domain where trace is enabled and one where it is not. Thus, Jackson is not related to a system which has two domains, both of which can controllably be monitored. As a result, it clearly cannot teach a system whereby the monitoring of one of the domains is controllably prevented as in Applicants' independent claims 1 and 20. Thus, Jackson fails to teach Applicants' claimed method step of "not allowing initiation of said monitoring function" and Applicants' claimed apparatus element of "control logic" to not "allow initiation of the monitoring function . . . ." As a result, Jackson

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clearly fails to disclose the subject matter of Applicants' independent claims 1 and 20 and claims

dependent thereon and thus any further rejection under 35 USC §102 is respectfully traversed.

Claims 1, 16, 17, 19, 20, 34, 35 and 37 stand rejected under 35 USC §102 as being

anticipated by Dayan (U.S. Patent 5,574,786). This rejection is respectfully traversed. As noted

above, Applicants' independent claims 1 and 20 are directed to processors which are capable of

operation in two domains and comprise the steps of (claim 1) and the logic elements (claim 20)

of monitoring the processor in both domains unless the monitoring function in one domain is

disabled.

The Examiner references column 16, lines 49-51 of the Dayan reference as allegedly

teaching the subject matter of Applicants' independent claims. The Examiner should appreciate

that in Dayan, the motion monitoring which is disclosed in column 16, lines 49-51, can only be

initiated in the secure mode (PAP installed). It cannot operate in the non-secure mode and

therefore is not the type of two domain processor which is the subject of the present invention.

Moreover, there is no control value as set out in Applicants' claim in the Dayan reference

that needs to be present if the monitoring function is to be initiated. Additionally, there is no

control value which is specifically related to a "condition." In Dayan, the monitoring function is

only allowed to be activated in the secure mode. This significant distinction is similar to that in

the Jackson reference, where in one domain the trace is not enabled and therefore no monitoring

occurs.

Neither Jackson nor Dayan envision monitoring in "at least two domains" and therefore

there is no need for disabling or not initiating monitoring based upon a "condition" and "its

related control value." As a result, Dayan cannot anticipate the subject matter of independent
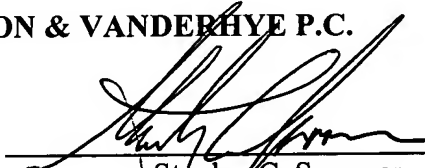
claims 1 and 20 or claims dependent thereon and any further rejection under 35 USC §102 is respectfully traversed.

Having responded to all objections and rejections set forth in the outstanding Official Action, it is submitted that claims 1-39 are in condition for allowance and notice to that effect is respectfully solicited. In the event the Examiner is of the opinion that a brief telephone or personal interview will facilitate allowance of one or more of the above claims, he is respectfully requested to contact Applicants' undersigned representative.

Respectfully submitted,

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